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The Risc V Esp32 Wifi

It ' s a WiFi SoC that
despite its ESP32 name
contains a RISC-V core
in place of the Tensilica

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core in the ESP32s we know, and uses the ESP8266 pin-out rather than that of its newer sibling.

Espressif Leaks

ESP32-C3: A WiFi SoC That ' s RISC-V And Is ESP8266 Pin- Compatible

NodeMCU first out of the gate with a pair of board designs, available

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now, built around AI
Thinker ESP32-C3
modules.

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NodeMCU Launches
Sub-\$5 Espressif
ESP32-C3 RISC-V
Development Boards

The question is what
should you compare this
to; is it more an ESP32 or
an ESP8266? The new
“ C3 ” variant has a
single 160 MHz RISC-V

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core that ... 5.0 in
addition to WiFi.

Hands-On: The RISC-V ESP32-C3 Will Be Your New ESP8266

Chinese tech startup
Huami Technology
today held its Next Beat
2021 conference at Hefei,
China. The conference is
themed “ The Future of
Health ” and indeed, the
company announced

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several new products ...

Huami unveils a new
Huangshan 2S chip & self-
developed Zepp OS for
smartwatches

Huami Technology
announced its own
hardware and software
products - the
Huangshan 2S chip and
Zepp OS for wearables.

Huami Technology

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Announced Its Own Huangshan 2S Chip And Zepp OS

It supports RISC-V
standard 'IMAC-FD'
extensions, Andes
contributed DSP/SIMD
'P' extension (draft), user-
level ... The
RivieraWaves Wi-Fi IP
family offers a
comprehensive suite of
IPs and platforms ...

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Risc-v processor IP

Listing

MPFS250 ” SBC that
runs Linux on

Microchip ’ s RISC-V
based, FPGA equipped
PolarFire SoC and offers
2x GbE, 2x FMC, 2x
micro-USB, PCIe x4,
CAN, HDMI, and
PMOD.

SBC builds on PolarFire
SoC with dual GbE and

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CAN Bluetooth

You might not have heard of the latter company, but it is a tech startup considered to be the leading designer of chips based on the open source RISC-V architecture. Several of the creators of ...

Intel pondering purchase of RISC-V chip designer SiFive

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or as a standalone UWB
MAC on commercial
Arm and RISC-V
MCUs. A flexible radio
interface enables the
RivieraWaves UWB
platform IP to be
deployed with customers'
own RF technology or
with CEVA ...

[CEVA Expands Its
Market-Leading Wireless
Connectivity Portfolio](#)

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with New Ultra- Wideband Platform IP

Industry-first combined IP offering of Bluetooth and UWB technologies is ideal for new use cases in emerging high-volume applications.

Low-Power UWB IP Meets Needs of IoT, Consumer Apps

If you have some time to install one of these and

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see if everything is running fine on your hardware, that would be great! The second announcement is that Haiku inc recently funded RISC-V hardware for ...

Haiku activity report - June 2021

The startup uses open RISC-V technology that competes with Arm and

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Intel, but Intel will build a test chip using SiFive's P550 design. Startup SiFive announced a faster new processor design ...

SiFive chip design challenges Arm and leads to Intel alliance

SEGGER ' s J-Link debug probes and Embedded Studio IDE now fully support Cudasip ' s RISC-V

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processors. J-Link ...
TomTom used
Infineon ' s AIROC
CYW43455 Wi-Fi and
Bluetooth combo chip to
add wireless ...

Week In Review: Design, Low Power

You can now run
Ubuntu Server on RISC-
V Canonical is looking to
help scale the ecosystem
around RISC-V FX's

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Alien TV show will start shooting next year – but it won't star Ripley Noah Hawley's ...

TechRadar news and features

Back in May, we reported on how Intel was using AI to add hyper-realistic visuals to GTA V. Now Intel is talking ... Thunderbolt 4 and Wi-Fi 6. Intel intros

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11th generation Core
processors for ...

Latest from Intel

The company seems primed to grow as it develops technologies such as 5G, Wi-Fi 6, and the 400 gigabit ... On another note, Intel has agreed to use SiFive ' s RISC-V IP in Intel ' s new foundries ...

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Best Stocks To Invest In
Right Now? 5 Tech
Stocks To Know

This may result in increased dependence on open-source Instruction Set Architecture (ISA) such as MIPS, RISC-V, etc. For example ...
Integrated Wi-Fi AX201,
and support for
Thunderbolt 3 and ...

Server Microprocessor

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Market Forecast to Reach
\$18.1 Billion by 2026

The RivieraWaves Wi-Fi IP family offers a comprehensive suite of IPs and platforms for embedding Wi-Fi connectivity into SoCs/ASSPs addressing a broad range of applications. This RW-N 802.11n ... The ...

TSMC Risc-v processor

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IP Listing

Imperas and Valtrix inked a multi-year distribution and support agreement that makes Imperas simulation technology and RISC-V reference models available pre-integrated within Valtrix STING for RISC-V ...

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