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~~Chapter 4 (Part I)~~

~~Basic Logic Design~~

~~Intro to Computer~~

~~Architecture Data~~

~~Structures | Important~~

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~~Architecture Objective~~

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architecture objective

questions include the

collections of

objective questions on

the fundamentals of

computer organization

and architecture. 1.

Memory unit that

communicates directly

with the CPU is called

the A) Main

memory B)

Secondary memory

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C) Auxiliary memory

D) Register. 2.

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Objectives: The

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Objectives of this course are: To understand how computers are constructed out of a set of functional units To understand how these functional units operate, interact and communicate

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Organization and

Architecture. In a k-

way set associative

cache, the cache is

divided into v sets,

each of which

consists of k lines.

The lines of a set are

placed in sequence

one after another. The

lines in set s are

sequenced before the

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lines in set $(s+1)$. The main memory blocks are numbered 0 onwards.

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are developing a high
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system to try to beat
the stock market.
Your system will
require a very high hit

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rate with low hit times in a medium-sized cache between main memory and the CPU. Because of the almost unlimited budget your firm has, the hit rate is prioritized over all other ...

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~~Organisation And~~

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~~Choice ...~~

2. SIMD represents an organization that _____. (A) refers to a computer system capable of processing several programs at the same time. (B) represents organization of single computer containing a

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Organization, processor unit and a memory unit. (C) includes many processing units under the supervision of a common control unit (D) none of the above.

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management. Each

quiz multiple choice

question has 4

options as possible

answers. Choose

your option and check

it with the given

correct answer.

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~~Questions with ...~~

2. SIMD represents
an organization that
_____. (A) Refers to a
computer system
capable of processing

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Several programs at the same time (B)

Represents

organization of single computer containing a control unit, processor unit and a memory unit (C) Includes

many processing units under the supervision of a common control unit

(D) None of the above

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~~Organization and~~

~~Architecture MCQ~~

~~Set 01 ...~~

In computer

engineering,

computer architecture

is a set of rules and

methods that describe

the functionality,

organization, and

implementation of

computer systems.

Some definitions of

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Architecture define it as describing the capabilities and programming model of a computer but not a particular implementation. In other definitions computer architecture involves instruction set architecture design, microarchitecture design, logic design,

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Organization and Implementation.

And

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The BSA instruction

is _____. (A) Branch

and store ... Objective

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Objective: This course is intended to teach the basics involved in data representation and digital logic circuits used in the computer system.

This includes the general concepts in digital logic design,

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including logic
elements, and their
use in combinational
and sequential logic
circuit design.

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Choice Question

(MCQ) 22. Shuseel

Baral.

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Objectives: Computer
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component of

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and the practicing

computer engineer

should have a

practical

understanding of this

topic. It is concerned

with all aspects of the

design and

organization of the

central processing

unit and the

integration of the CPU

into the computer

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