

80386 Instruction Set

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Chapter 17 80386 Instruction Set. This chapter presents instructions for the 80386 in alphabetical order. For each instruction, the forms are given for each operand combination, including object code produced, operands required, execution time, and a description. For each instruction, there is an operational description and a summary of exceptions generated.

80386 Programmer's Reference Manual -- Chapter 17

Chapter 17 80386 Instruction Set This chapter presents instructions for the 80386 in alphabetical order. For each instruction, the forms are given for each operand combination, including object code produced, operands required, execution time, and a description. For each instruction, there is an operational description and a summary of ...

Chapter 17 80386 Instruction Set - logix.cz

The Intel 80386, also known as i386 or just 386, is a 32-bit microprocessor introduced in 1985. The first versions had 275,000 transistors and were the CPU of many workstations and high-end personal computers of the time. As the original implementation of the 32-bit extension of the 80286 architecture, the 80386 instruction set, programming model, and binary encodings are still the common denominator for all 32-bit x86 processors, which is termed the i386-architecture, x86, or IA-32, depending o

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ENHANCED INSTRUCTION SET OF A 80386 PROCESSOR 1. Bit Scan Instructions: 80386 instruction set has two bit scan mnemonics, such as BSF (bit scan forward) and BSR (bit... 2. Bit Test Instructions: 80386 have four bit test instructions, those are BT (test a bit), BTC (test a bit and... 3. Conditional ...

MICROPROCESSORS: ENHANCED INSTRUCTION SET OF A 80386 PROCESSOR

16.1 How the 80386 Implements 16-Bit and 32-Bit Features; 16.2 Mixing 32-Bit and 16-Bit Operations; 16.3 Sharing Data Segments Among Mixed Code Segments; 16.4 Transferring Control Among Mixed Code Segments> Part IV Instructions Set Chapter 17 -- 80386 Instruction Set. 17.1 Operand-Size and Address-Size Attributes; 17.2 Instruction Format. Appendices

80386 Programmer's Reference Manual -- Table of Contents

The 8086 / 80286 / 80386 / 80486 Instruction Set This HTML version of the file intel.doc from the PC Game Programmer's guide was produced by Zack Smith, fbui@comcast.net. ... If CF is set, a 1 is added to the destination. Clocks Size Operands 808x 286 386 486 Bytes reg,reg 3 2 2 1 2 ...

The 8086 / 80286 / 80386 / 80486 Instruction Set

80386 Instruction Set 1. Arithmetic Instructions. There are two new sign extension instructions beyond those of the 8086. CWDE Sign-extend 16... 2. Bit Instructions. BSF scans (checks) the 16-bit (word) or 32-bit (double word) number defined by s from right to left... 3. Set Byte on Condition ...

80386 Instruction Set ~ 8051 microcontrollers

INTEL 80386 PROGRAMMER'S REFERENCE MANUAL 1986 Page 1 of 421 INTEL 80386 PROGRAMMER'S REFERENCE MANUAL 1986 Intel Corporation makes no warranty for the use of its products and

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The updated instruction set is also grouped according to architecture (i386, i486, i686) and more generally is referred to as x86 32 and x86 64 (also known as AMD64). Original 8086/8088 instructions Added in specific processors Added with 80186 / 80188

x86 instruction listings - Wikipedia

Following is the list of instructions under this group ? REP ? Used to repeat the given instruction till CX ? 0. REPE/REPZ ? Used to repeat the given instruction until CX = 0 or zero flag ZF = 1. REPNE/REPZ ? Used to repeat the given instruction until CX = 0 or zero flag ZF = 1.

Microprocessor - 8086 Instruction Sets - Tutorialspoint

17.2.2 How to Read the Instruction Set Pages The following is an example of the format used for each 80386 instruction description in this chapter: CMC -- Complement Carry Flag Opcode Instruction Clocks Description F5 CMC 2 Complement carry flag

80386 Programmer's Reference Manual -- Section 17.2

 They gave up, when Pentium solved the issue (by internally translating CISC -> RISC). en lieu et place de leur contrepartie en 16 bits (ax, bx, etc.).. That also failed — big time — against the simpler upgrade path offered by AMD. The multiplication instructions set carry and overflow if the result is larger than the small value register. Although the 80386 does not perform out-of ...

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Returning from a Nested Task: If you execute an IRET instruction when the NT (Nested Task) flag in EFLAGS is set, the 80386 returns to your “parent” task. However, it will neglect to clear NT in the

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outgoing task's TSS.

Intel 80386 CPU Information | PCjs Machines

An 80386 data-manipulation instruction that accesses memory uses one of the following methods for specifying the offset of a memory operand within its segment: Most data-manipulation instructions that access memory contain a byte that explicitly specifies the addressing method for the operand.

80386 Programmer's Reference Manual -- Section 2.5

This section presents an overview of the 80386 processor as it applies to assembly language programming. Most books you will find on this subject are geared toward PC based assembly language products using the Intel 803x6 instruction set. The GNU assembler (GAS), which DJGPP uses, is based on the AT&T 680x0 instruction set.

Guide: About the 80386 architecture

LOOP/LOOPcond -- Loop Control with CX Counter Opcode Instruction Clocks Description E2 cb LOOP rel8 11+m DEC count; jump short if count > 0 E1 cb LOOPE rel8 11+m DEC count; jump short if count > 0 and ZF=1 E1 cb LOOPZ rel8 11+m DEC count; jump short if count > 0 and ZF=1 E0 cb LOOPNE rel8 11+m DEC count; jump short if count > 0 and ZF=0 E0 cb LOOPNZ rel8 11+m DEC count; jump short if count > 0 ...

80386 Programmer's Reference Manual -- Opcode LOOP

The 80x86 family provides five logical instructions, four rotate instructions, and three shift instructions. The logical instructions are and, The 80386 and later processors provide an even richer set of operations. bts, btr, btc, bsf, bsr, shld, shrd, and the conditional

CHAPTER SIX: THE 80x86 INSTRUCTION SET (Part 3)

The Instructions used for data transfer in I/O mapped I/O are a) IN, OUT b) IN, LDA add c) STA add d) None of the above 20. Number of Address lines in 80486 is a) 16 b)32 c) 34 d) 128 REVIEW QUESTIONS 2 MARKS 1. Differentiate between 80386 and 80486. 2. Classify the different groups of 80386 instruction set with example. 3.

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