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~~Resonance: CD4046BE~~

~~Phase Locked Loop~~

~~Resonance Demo Little~~

~~Boxes Part 7: 4046~~

VCO #60: Basics of

Phase Locked Loop

Circuits and Frequency

Synthesis Simple Phase

Locked Loop

Application Demo

78. The PLL as a FM

Demodulator

\$1 MHz Oscillator 4046

PLL Chip 77. PLLs as

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Frequency Multipliers

76. Phase Locked

Loops Julian plays with
Frequency Division and

Multiplication A Basic

Look at the PLL Circuit

Phase Locked Loop

Tutorial | PLL Basics

What is Phase Lock

Loop (PLL)? How

Phase Lock Loop Works

? PLL Explained

resonance frequency of

a coil ~~Let's make a~~

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~~Voltage Controlled~~

~~Oscillator (VCO)~~

*Introduction to Phase
Locked Loops*

Electronic Basics #17:

*Oscillators || RC, LC,
Crystal Synth Voltage
Controlled Oscillator*

*1v/oct tracked DIY how
to. #55: Back to Basics:
Tutorial on LC*

*Resonant Circuits VCO
Audio Applications Part
1- Synthesizer Building*

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*Block (Voltage
Controlled Oscillator)*

4046 PLL as frequency
multiplier and lock onto
18.3khz....39.2khz

Modifying a 4046 based
VCO Classic 2 Op-Amp
tri and square wave
VCO - DIY Modular in
a Week 1.2

First 4046 voltage
controlled oscillator
circuit VCO
oscilloscope and LED

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circuit CD4046

CD4046B 4046 PLL

Oscillator: 4066 Sample
and Hold Freakout **DIY**

Phase Locked Loop

VCO

187N. Intro. to phase-
locked loops (PLL)

noise **4046 VCO - DIY**

Modular in a Week 1.3

PLL 4046 Organ

Synthesizer 4 minute

Jam Engineer It: How

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to design with excellent
PLL \u0026amp; VCO noise
performance CD4046
200Hz 200Khz

Oscillator Easy Easy
Easy 4046 Pll Circuit
Design

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Redkmasramorles PLL

using 4046 - Phase

Locked loop. CD4046 is

a PLL or phase lock

loop, it mainly consists

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of a VCO and phase comparators. This is a component in FM demodulation and modulation. Mixed and Interface Circuits. It is used in a closed loop control to maintain a stable frequency.

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Redkmasramorles PLL using 4046 - Phase Locked loop. CD4046 is a PLL or phase lock loop, it mainly consists of a VCO and phase comparators. This is a component in FM demodulation and modulation. Mixed and Interface Circuits. It is used in a closed loop control to maintain a stable frequency. The

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Circuit above is

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using 4046 - Phase

Locked loop. CD4046 is

a PLL or phase lock

loop, it mainly consists

of a VCO and phase

comparators. This is a

component in FM

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demodulation and
modulation. Mixed and
Interface Circuits. It is
used in a closed loop
control to maintain a
stable frequency. The
Circuit above is good
for ...

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Figure 2 shows a block diagram of the CD4046B, which has been implemented on a single monolithic integrated circuit. The PLL structure consists of a low-power, linear VCO and two different phase comparators,

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having a common signal-input amplifier and a common comparator input. A 5.2-V Zener diode is provided for supply regulation, if necessary.

~~CD4046B Phase-Locked Loop: A Versatile Building Block for ...~~

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Locked loop. CD4046 is

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a PLL or phase lock loop, it mainly consists of a VCO and phase comparators ...

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The HC/HCT4046A

PLL with VCO is a high-speed CMOS IC

designed for use in general-purpose PLL

applications, including

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frequency modulation,
demodulation,
discrimination,
synthesis, and
multiplication.

~~CMOS Phase-Locked-
Loop Applications (Rev.
B)~~

Phase-Locked Loop
Design Fundamentals
Application Note, Rev.
1.0 2 Freescale
Semiconductor with the

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reader. Since the scope of this article is practical in nature all theoretical derivations have been omitted, hoping to simplify and clarify the content. A bibliography is included for those who desire to pursue the theoretical aspect.

~~Phase-Locked Loop
Design Fundamentals~~

The 4046 chip is a phase-

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locked loop. A phase locked loop IC consists of a voltage-controlled oscillator (VCO) and a phase detector. In this circuit, we will use only the VCO portion of the 4046 IC and not the phase detector. We use the chip just to generate a digital signal that can be used a clock signal.

~~How to Build a Voltage~~

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controlled Oscillator (VCO) Circuit ...

The IC 4046 is a Phase-locked loop IC of CMOS digital (combined analog and digital chip). A Phase-locked loop (PLL) has a voltage-controlled oscillator (VCO). The VCO signal and an input signal are sent to a phase comparator which generates an error

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voltage part to any
difference in frequency
between two signals.

~~CD4046 circuit~~

~~datasheet (phase-locked
loop) | ElecCircuit.com~~

Figure 2 - functional
diagram of the 4046
phase-locked-loop with
vco The exact ranges
and component values
are determined by
extensive charts

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included in the 4046
data sheet - (443K) in
PDF format. This gives
us a very flexible VCO
capable of operating
anywhere up to 17 Mhz,
something the early
CMOS versions were
incapable of doing.

~~74HC4046 phase locked
loop electronics
tutorials~~

CD4046 Ten Times 10×
Page 26/30

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Frequency Multiplier

Circuit. For 1Hz to 1KHz input range, we design a VCO to cover 10Hz to 10KHz, with some extra range on each end. the VCO output is divided by 10 and then compared to the input signal using the wideband phase detector. The phase-detector output goes to the loop filter and then

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reaches around and
closes the loop by
running the VCO.

~~4046 Ten Times Frequency Multiplier Circuit | Electronic ...~~

There are many applications of a PLL circuit. PLL circuits can demodulate an FM frequency, multiply frequencies, synchronize processor

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4046 PII Circuit

clocks, and a host of other things. The inside of a 4046 IC...

~~Phase-Locked Loop:~~

~~FM Demodulator-~~

~~ELEC101~~

The Design of Phase-Locked-Loop Circuit for Precision Capacitance ...

J., "Low power consumption

phaselocked loop 4046

applied research and

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simulation," Popular
science technology, Vol.
16, Issue ...
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